Silicon Quantum Photonics

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Abstract—Integrated quantum photonic applications, providing physically guaranteed communications security, sub-shot-noise measurement, and tremendous computational power, are nearly within technological reach. Silicon as a technology platform has proven formidable in establishing the micro-electronics revolution, and it might do so again in the quantum technology revolution. Silicon has taken photonics by storm, with its promise of scalable manufacture, integration, and compatibility with CMOS microelectronics. These same properties, and a few others, motivate its use for large-scale quantum optics as well. In this paper, we provide context to the development of quantum optics in silicon. We review the development of the various components that constitute integrated quantum photonic systems, and we identify the challenges that must be faced and their potential solutions for silicon quantum photonics to make quantum technology a reality.

Index Terms—Quantum optics, silicon photonics.

I. INTRODUCTION

The unique behaviour of quantum systems, exhibiting such properties as superposition and entanglement, can be harnessed to process, transmit, and encode information. Quantum information science promises to revolutionize information technology, including the communication [1], [2], processing [3], [4], and collection [5] of information. Photons—single quanta of light—and optics are at the forefront of this quantum revolution [6].

Integrated optics has unlocked new levels of scale and performance in quantum optics. Early demonstrations used glass-based waveguides to achieve high-fidelity on-chip quantum interference and quantum logic operations [7], multi-particle quantum walks [8], the reconfigurable generation and manipulation of entanglement [9], and bosonic sampling [10]–[12]. However, these glass-based approaches to integrated quantum photonics are already reaching the limitations of scalability and circuit complexity—with even simple circuits requiring 10 cm devices, and with little hope for scaling up in complexity and functionality. Silicon photonics promises to allow quantum optics to scale to new heights. Many of the qualities which allowed silicon to prevail as the dominant material of electronics are the same qualities which afford it a second glance optically. Silicon is abundant, has excellent thermal conductivity, is mechanically robust, can be made unimaginably pure, can be doped, and—most importantly—has an inert oxide with which it forms high-quality interfaces. Silicon waveguides are formed by etching the device layer of a silicon-on-insulator (SOI) wafer, with confinement provided by the buried oxide (BOX) underneath and a capping oxide above. Wafers with a 220-nm thick silicon device layer and a 2-μm thick BOX have become standard for photonic structures for use in the 1.55-μm telecommunications C-band. These wafers yield tightly-confining waveguides with a typical cross-section of 220 × 450 nm², with bend radii around 5 μm and an associated large integration density [13], [14].

The promise of an optical interconnect, whereby communications between electronic processor cores and between processor cores and memory [15] are handled optically—increasing range and bandwidth—has strongly motivated the development of silicon photonics so far. For the same reasons of scale, functionality, and manufacturability, silicon photonics may give us a crucial edge in building future photonic quantum devices.

Each quantum application—communication, sensing, computation, etc.—places its own set of requirements on the underpinning photonic technology, but these applications also have many requirements in common. Fig. 1 shows how a generic silicon quantum photonic device might fit together. All quantum applications need a source of single photons, with various photon statistics. All applications then require those photons to be manipulated—either to encode information, to make measurements, or to prepare a particular quantum state—using active optics to coherently mix photonic modes, and active optics and optical delay lines to reconfigure those modes on the fly. Once the photons have performed their useful evolution, we must always extract some classical information about their resulting state, and we do this using detectors sensitive to single photons. To prevent these detectors from saturating in the presence of bright pump fields—needed by many quantum light sources—we require very high extinction filters, to separate single-photon signals from these bright fields. Finally, many applications would benefit from efficient and broadband fibre-to-chip couplers, to aid in implementing those functions more naturally accomplished off-chip, such as delay lines, and the provision of high-power pump lasers. All these building blocks have now been demonstrated separately, and with mixed performance, by the silicon quantum photonics community. The challenge for future work, then, is clear: to improve the performance of individual blocks to levels sufficient for each quantum application, and to integrate these high-performance blocks on a common silicon substrate.

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Silicon is a powerful workhorse for achieving the goals of quantum photonics, but is not without its limitations. When it comes to component-level development and integration, the silicon material system is pre- eminent, with exponential improvements in microelectronics in both development and integration for the better part of the past century. Optically, silicon benefits from a strong third-order ($\chi^{(3)}$) nonlinearity, whereby the material refractive index varies with the optical intensity. This enables a wide range of devices, from photon sources (Section II-A) to photo-optic switches (Section II-D). On the other hand, nonlinear two-photon absorption (TPA) is the Mr. Hyde to the third-order nonlinearity’s Dr. Jekyll. It presents a challenge for high-powered nonlinear optics, leading to parasitic free-carrier and thermal effects. The small size and high confinement of silicon waveguides makes their properties vulnerable to small differences in fabrication—most loss in SOI waveguides, for instance, arises from the roughness of their etched sidewalls, rather than from any intrinsic absorption effect [16], [17]. Despite these challenges, the community has made remarkable progress. We review this progress in Section II.

II. QUANTUM PHOTONIC DEVICES

A. Photon Sources

Many techniques exist for the production of quantum light and single photons [18]; which one is most suitable depends on the application. One single photon is well approximated by attenuated laser light, for example, where a pulse’s chance to contain more than one photon is attenuated faster than its chance to contain only one. This fact is widely used in quantum key distribution systems [19], [20] which encode information on single photons. True single photons, on the other hand, can be obtained directly from atom-like emitters, such as trapped ions or quantum dots [21]. Tremendous progress has been made over the past few years in improving the specifications of true single-photon emitters. They remain, however, hard to manufacture and do not offer much spectral flexibility. Notably, quantum dots have recently been integrated with other on-chip quantum optics [22], and have obtained high levels of single-dot pulse-to-pulse indistinguishability [23], bringing them nearer to system-level integration.

Pairs of photons can be spontaneously created in silicon, owing to its nonlinear optical properties. Two photons from a bright pump laser can be spontaneously shifted to different wavelengths via an elastic process called spontaneous four-wave mixing (SFWM). These two photons, often referred to as signal and idler, are quantum correlated, preserving the phase of the original pump. Due to silicon’s crystallinity, noise from spontaneous Raman scattering is localized at exactly 15.6 THz from the pump frequency, making this noise easier to engineer against than in other systems, like silica fibre and chalcogenide glasses [24]. If generated in a monomode waveguide, the signal and idler photons will emerge in that waveguide’s single transverse mode, eliminating mode matching and photon collection issues. Silicon SFWM sources also integrate naturally with passive optics, as they are formed in the same silicon patterning and etching steps.

Compared with the true single photon emitters, SFWM sources have the following drawbacks: the quantum state produced is a squeezed state which can approximate a photon pair only if the pump laser is relatively weak. This makes the generation process probabilistic: each time a pump pulse is used to seed the source, either no pair is produced, a single pair is produced, or multiple pairs are produced (a noise source in many quantum information tasks). An ideal photon-pair source is operated with thermal statistics [25] which suppress correlations between signal and idler photons. These correlations are responsible for a decreased purity when implementing heralded single-photon sources.

Assuming an ideal photon-pair source, which emits into only two spectral modes, the probability to obtain $n$ pairs per pulse is given by [25], [27], [28]

$$p_n = \left(\text{sech}(|\xi| \cosh n |\xi|)\right)^2 \left(\Delta \nu / \Delta \nu_p\right)^n$$

(1)

Several statistics, from thermal to Poissonian, may arise, depending on the conditions of operation [25], [26].
where $\xi$ is the squeezing parameter and $|\xi|^2$ is the pair generation probability when the pump power is low, $\Delta \nu$ is the full emission bandwidth, and $\Delta \nu_c$ is the collection bandwidth ($\Delta \nu_c \leq \Delta \nu$). The probability to emit a single pair reaches a maximum around 25\%.

This type of source is not scalable—the probability for $N$ sources to each emit exactly one pair decreases exponentially with $N$. However, these sources can be used to construct near-deterministic single-photon sources, with the addition of single-photon detectors, a switching network, and suitably fast control electronics [29]—in a so called ‘source multiplexing’ configuration.

SFWM depends on several parameters. The bandwidth of the generated photons is governed by energy conservation, the pump bandwidth, phase matching (dependent on the waveguide dispersion), and the presence or absence of a cavity [31]. Since SFWM consumes two pump photons, its efficiency (when the pump is relatively weak) grows quadratically with pump power, as shown in Eq. (2), below. Provided the power is kept low enough to neglect multi-pair emission, the pair generation rate is [32]

$$|\xi|^2 \approx \gamma^2 L_{\text{eff}}^2 P^2 \Theta^2.$$  \hspace{1cm} (2)

$\gamma = n_2 k_0 / A$ is the nonlinear parameter, with $n_2$ the nonlinear refractive index, $k_0$ the vacuum wavenumber, and $A$ the effective mode area [33]; $L_{\text{eff}} = (1 - e^{-\alpha L}) / \alpha$ is the effective interaction length accounting for linear loss $\alpha$; $P$ is the pump power; $\Theta = \text{sinc}(\Delta k L)$ is the phase matching coefficient, with $\Delta k$ being the momentum mismatch between the four fields.

While SFWM is efficient and useful in silicon waveguides, other less useful nonlinear phenomena also occur. Foremost among these is two-photon absorption (TPA), which increases the single-photon propagation loss both directly, via cross two-photon absorption (XTA) where one single photon and one pump photon are absorbed together to excite an electron, and indirectly, by being absorbed by one of the free carriers generated in this way (FCA). These effects have been modelled and carefully observed in straight waveguide pair generation experiments [32]. The pair generation rate is no longer quadratic with the pump power, but scales as

$$|\xi|^2 \xrightarrow{\text{TPA}} \gamma^2 A^2 \alpha_2^{-2} \log(1 + \alpha_2 P L_{\text{eff}} / A) \Theta^2$$ \hspace{1cm} (3)

where $\alpha_2$ is the TPA coefficient. This leads to a saturation in the pair generation rate due to TPA. This model is used to describe measured data in Fig. 2(a).

Resonators improve several aspects of the photon-pair source. First, they typically have a much smaller footprint than straight- or spiralled-waveguide sources, allowing a higher integration density. Second, they enhance the brightness by increasing the circulating intensity of the resonant pump, thereby reducing the pump power required to achieve a given pair generation rate. Third, they affect the joint spectral emission of the photon pair, forcing the signal and idler to each have a spectrum centered on a resonant cavity mode. By adjusting the pump bandwidth, one can also ensure that pairs of emitted photons are spectrally separable (uncorrelated) [34], thus making the resonant source a good candidate for the heralding of single photons. This is supported by measurements of the joint spectral intensity of silicon ring sources [35], [36], and by measurements of the correlation function of pairs generated in silicon microdiscs [37] and in hydex and silicon nitride rings [38], [39].

In a resonator, the phase matching condition is naturally satisfied, as cavity resonances are evenly spaced in wavenumber. Energy conservation still applies, however, and a source cavity must be triply resonant: for the consumed pump, and for the generated signal and idler photons. This is typically the case for neighbouring resonances in silicon rings, for which the dispersion is roughly linear and so the cavity resonances are roughly evenly spaced in frequency as well as in wavenumber.\(^2\) On the other hand, operating silicon resonators requires extra care: both TPA and FCA have associated phase effects which can modify the cavity resonances as a function of the intra-cavity power. The exact impact of these phenomenon on the biphoton joint spectral amplitude, and its potential impact on the purity of a heralded single photon source is a topic of current research.

Photon-pair generation in a silicon nanowire was first reported by Sharping et al. in 2006 [40]. Since then, many SFWM experiments have been reported: in strip waveguides [40]–[43], with control over polarization [44] (Fig. 4(d)), in a phase stable

\(^2\)As a result, energy is conserved ($2\nu_p = \nu_s + \nu_i$), in addition to momentum, which is always conserved for equally spaced resonances ($2k_p = k_s + k_i$).
Fig. 3. (a) Bright beam is injected and split on the first MMI, then pumps simultaneously the top and bottom spiral arms of the MZI source, generating a pair in superposition of being on the bottom or top arm which interferes on the last MMI. By varying the MZI phase, the two-photon output state can be tailored to bunch in one output or split between two outputs. (b) Two-photon fringe obtained when monitoring the two-photon coincidences between two different outputs.

Fig. 4. Examples of structures used for the generation of photon pairs: (a) photonic crystal waveguide, (b) coupled photonic crystal cavities, (c) ring resonator, (d) waveguide with polarisation rotator, (e) two-path source with a 2D-grating combiner, (f) coupled ring resonator optical waveguide, (g) coupled one-dimensional photonic crystal resonators, (h) microdisk resonator, (i) bidirectionally, non-degenerately pumped all-pass ring, and (j) bidirectionally, non-degenerately pumped add-drop ring. See main text for references.

interferometer with [45] (Fig. 3) and without [46] (Fig. 4(e)) tunable elements. Multiple types of resonator-based sources have also been investigated: single-ring resonators [30], [35], [47]–[51] (Fig. 4(c)), including studies of the effect on reverse bias PIN junctions for mitigating FCA [30], [52] (Fig. 2(b)); ring resonators in a self-locking double-bus configuration [53] (Fig. 4(j)), in a quantum splitter configuration [54] (Fig. 4(f)), and in coupled-resonator optical waveguides (CROW) [36], [55] (Fig. 4(f)); high-Q microdisks [37], [56] (Fig. 4(h)); photonic crystal waveguides [57], [58] (Fig. 4(a)), and coupled cavities [59], [60] (Fig. 4(b)); and in one-dimensional photonic crystal resonators [61] (Fig. 4(g)).

B. Passive Optics

Once a quantum state of light has been generated, quantum information is mapped onto and off of this state coherently, using passive, linear optics. In some applications, these passive optics are relatively simple; in others, they form intricate nested interferometers. QKD can function with as few as one Mach-Zehnder interferometer at the transmitter and one at the receiver, to share a secure quantum key. Linear optical quantum computation, on the other hand, is likely to require thousands or millions of passive optical elements [64].

The most complex quantum photonic device reported to date consists of 30 directional couplers enclosing 30 thermal tuners on 6 modes, etched into glass waveguides on a single 40 × 100 mm² die [65]. Boson sampling, whereby a large multiphoton state is measured after propagating through a large interferometer, thereby sampling the bosonic distribution, may represent the first opportunity to show that quantum devices are classically unsimulable [66]. Several groups have reported cm-scale laser-written waveguide implementations of the boson sampling experiment, with 10, 12, and 72 waveguide couplers ([10] and [11], [67], and [68], respectively). These impressive results start to run up against the limits of what is possible on a single substrate. Furthermore, none of these laser-written devices incorporated electro-optic tuning elements. A higher component density is the only way to grow these devices further, and, though they excel at producing low-loss passives, these glass-based integrated optics lack facilities for doing much else.

Silicon waveguides offer solutions to these problems, with unmatched component densities, and added functionalities based on silicon’s semiconducting nature. The quantum optics community has already begun to exploit the density and reproducibility of silicon passive structures. A silicon photonic device with 112 multi-mode interferometers (MMIs) and 213 silicon-based tuners (Fig. 5) has recently been demonstrated by Harris, Steinbrecher et al. [62], [63], with a scale substantially exceeding those of the aforementioned glass-based devices. Experiments on this device have so far used only bright light, though its design is for single photons.

Quantum optics, based on the passives available in silicon photonics, has advanced rapidly in recent years. After the generation of photon pairs in silicon waveguides was demonstrated in 2006 [40], the first quantum interference in silicon photonics was demonstrated in 2012. Bonneau et al. used 2 × 2 MMIs to construct a classic Hong-Ou-Mandel (HOM, [69]) interferometer (shown in Fig. 6), and a MZI, and used these two experiments to show quantum interference visibilities exceeding 80% [70]. Subsequently, Xu et al. extended this work to directional couplers, showing a HOM dip with a visibility of 90.5% [71]. Silverstone et al. showed that SFWM photon-pair sources were straightforward to integrate with other passive
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Fig. 5. Programmable nanophotonic processor, from reference [62], [63]. A Mach-Zehnder interferometer (MZI) is highlighted. The device contains 56 such interferometers, and 213 phase tuners to control their interferences. Image credit: N. C. Harris.

Fig. 6. (a) Schematic of a two-photon quantum interference experiment. Two photons are launched in a silicon MMI. A tunable delay line at the input allows for varying the relative arrival time (delay) between the two photons. Each output port of the MMI is monitored with a single photon detector and correlated detection events (coincidences) are recorded. (b) Number of coincidences as a function of the delay. A drop in the coincidences is observed as the wavepackets of the two photons overlap, leading to destructive interference in the state where they exit different output ports.

optics in 2013 [45], obtaining on-chip quantum interference visibilities approaching unity (see Fig. 3). This development lead to the demonstration of entangled single-qubit logic in 2015 [35], again with photons generated on chip, but this time powered by ring-resonator-based photon-pair sources (see Fig. 7). Also in 2015, Silverstone, Santagati et al. demonstrated multi-qubit logic applied to on-chip-generated entangled photons [72], in a device with 21 passive elements, 16 thermal tuners, and 4 photon-pair sources (producing one pair in superposition between them). Silicon photonics offers passive optical components with high density, high yield, high performance, and which can be combined with extra on-chip functionalities. These robust components will form the backbone of any future silicon quantum photonic computer, communications system, or metrology device.

Fig. 7. On-chip Bell state generation and analysis, from reference [35]. (a) Optical micrograph of the device, with components annotated: directional coupler (DC), wavelength division multiplexer (WDM). A bright pump generates non-degenerate photon pairs in superposition between two resonant SFWM sources. These pairs are reconfigured and analyzed on-chip in the path qubit basis. (b) An on-chip-reconstructed two-qubit entangled state. Image credit: M. J. Strain.

C. Single-Photon Detectors

Quantum opticians have always worked in the wavelength bands least limited by photon sources, optics, and single-photon detectors. Initial experiments sourced correlated photons from atomic ‘cascades’ in sodium [75] and calcium [76] vapours, which emit visible photons. Visible-wavelength optics were also easily sourced. Later, with the advent of parametric down-conversion photon-pair sources and silicon avalanche photodiode (APD) detectors, experiments shifted to the region of maximum silicon absorption and APD sensitivity, around 800 nm. Recently, driven by the enormous optical telecommunications industry, activity has been building in the telecoms band around 1.55 μm. Here, long-range transmission over optical fibre is possible, and the low-cost and high-quality tools of the telecoms industry can be exploited—lasers, (bright-light) detectors, and optics in bulk and fibre. This region is also amenable to waveguides made of silicon, being within silicon’s 1100-nm bandgap. It does present a single-photon detection problem, however, as the community’s silicon-APD workhorse is not sensitive in this spectral region.

At cryogenic temperatures,3 the recently developed superconducting nanowire single-photon detectors (SNSPDs, Fig. 8(a), [77], [78]) are sensitive at a wide range of wavelengths, including in the 1.55 μm band. They realize near-ideal

3In the range of 1 to 10 K, where non-dilution Gifford–McMahon fridges are relatively cheap and readily available.
In 2011 [102], membrane, then—after verifying its performance—they have demonstrated a delay line with propagation loss and group, respectively. Delay lines are discussed in [74] (shown in Fig. 8(c)) modulators have proven themselves to in silicon. This can (a) and (d)) could fill the role, with sufficient performance.

D. Optical Switches

LOQC was not taken seriously until 2001, when Knill, Laflamme, and Milburn discovered that by adding extra photons, and using feedforward, one could effectively implement the missing nonlinear interactions required to implement a universal gate set [90]. In practice a feedforward step requires reconfiguration of a large photonic network in a short time frame. A fast, low loss, low noise, non-blocking optical switch has a central role to play in the LOQC architecture [64], [90].

A simple example of feedforward is shown schematically in Fig. 9. Here, a detector measures a heralding photon (latency $\tau_d$, efficiency $\eta_d$) and actuates an optical switch (settling time $\tau_s$, transmission $\eta_s$), while a heralded photon is delayed by $\tau = \tau_d + \tau_s$ in a delay line with propagation loss and group velocity $\alpha$ and $v_g$, respectively. Delay lines are discussed in detail in Section II-G. The heralded photon is transmitted with efficiency $\eta = \eta_d \eta_s \exp[\alpha \tau v_g]$, showing the tradeoff between efficiency and speed in detector, delay, and switch.

Heat- and MEMS-based phase tuners (e.g. [91]–[93]) are extremely versatile when it comes to reconfiguring large complex networks. However, they lack the speed (operating at most in the MHz regime) for rapid, time-of-flight feedforward applications, like the one outlined above.

Electro-optic ($\chi^{(2)}$) modulators have proven themselves to be strong candidates, with a response time limited only by their driving electronics—they are routinely operated at 40 Gb/s [94]. They are also capable of handling quantum states of light, with no additional inherent loss from the electro-optic effect [95], [96].

Silicon, however, being a centro-symmetric crystal, naturally has $\chi^{(2)} = 0$. The usual approaches for implementing switches in silicon rely on modulating carrier concentrations in the waveguides to change the refractive index [97], offering switching rates in the tens of GHz. Unfortunately, these effects are accompanied by phase-dependent loss, and by noise when operated in a forward-biased configuration [98], [99]. Nonlinear optics can be leveraged to implement all-optical switching, either relying on optical free-carrier generation [100], or on the Kerr effect and cross-phase modulation (XPM) [101]. These switch architectures, however, suffer an increased complexity arising from the need to add and remove a bright optical pump. A fibre implementation of this scheme was used to switch single photons by Hall et al. in 2011 [102].

Another approach is to induce a $\chi^{(2)}$ in silicon. This can be achieved by breaking silicon’s lattice symmetry using strain
indicate progressive levels of on-chip integration. Typical μet al. 2–5 μphotons (materials can be directly integrated with around 10 1 nJ) which compose each i that filter in the cladding, 40 Gb/s rates [106], et al. 6700113 by one of the ca. must be made to ensure that such detectors are not triggered the same subsystem, and likely on the same die. Every effort pair sources and single-photon detectors in close proximity—in F. Fibre-to-Chip Coupling

Efficient coupling between the nanoscopic silicon waveguide core and a standard optical fibre has been a key challenge since the early days of silicon photonics. Two main methods have emerged. One, edge-coupling, requires a small-mode lensed fibre (∼ 2–5 μm spot) to couple light into an adiabatic spot-size converter [113]. The specialty fibre and small mode size make it impractical couple multiple fibres at the same time using this method. A second method involves fabricating a second-order grating, to vertically couple light directly between a cleaved optical fibre and a waveguide [114], [115]. This method has an inherently limited bandwidth, due to the grating, but these gratings can be densely placed and accessed with multi-core fibres and v-groove array packages. Both methods have shown tremendous efficiency improvements, with per-channel coupling losses now below 1 dB [116], [117]. Low-loss interconnectivity could enable quantum key distribution between silicon transceivers,
and frees quantum system architectures from strictly monolithic integration.

Both types of couplers are routinely used for injecting pump fields and for collecting single photons. Polarization-splitting two-dimensional grating couplers [118] are an appealing solution for converting path-encoded qubits (stable on a monolithic chip, but not in fibre) to polarisation-encoded qubits (better suited for short fibre and free-space links). Olislager et al. reported an entangled photon-pair source using a two-dimensional grating [46], and Wang et al. used this structure for chip-to-chip quantum communication [119].

**G. Delay Lines**

From time-bin encoded QKD [120] to one-way quantum computing [121], many photonic implementations of quantum information tasks require memory. Storing a photon for an arbitrary amount of time, via a quantum memory, is a challenging task. This can be achieved, for example, using nonlinear interactions [122], [123] but such memories are, for now, difficult to manufacture and integrate, and have a significant control overhead. Plasma dispersion based tunable delay lines have been demonstrated on silicon [124], but they have inherent losses and their single-photon operation has not yet been tested. Discrete programmable delay lines can be made from several different optical structures [125]: CROWs [126], photonic crystals [127], or sequences of unbalanced MZIs [128]. The latter approach has been used in a recent QKD demonstration [20].

In many applications—including in a quantum repeater [129], QKD [120], and one-way quantum computing [130]—a fixed duration memory is enough. Quantum states of light can simply be stored in a long waveguide. Low-loss delay lines (0.024 dB/ns) compatible with the silicon platform have been developed [131], [132] with demonstrations of delays of up to 130 ns. With fibre-to-chip coupling efficiency continuing to increase (see previous section), low-loss optical fiber may represent the ultimate delay solution. While the footprint of a delay line can be large, this type of memory has inherently high bandwidth, no noise, no requirement for active control, and it works in all environments, from millikelvin to room temperature.

**III. OTHER PLATFORMS**

Silicon photonics is a strong contender for the quantum photonics crown, but is not without able challengers. In Table I, we qualitatively and quantitatively compare silicon, as a platform for quantum optics, with several other leading technology platforms. This comparison is not exhaustive, and is by necessity subjective; we have tried to make it quantitative wherever possible. We assess the six main platforms used for on-chip quantum optics—silicon, etched silica, direct-write, silicon nitride, indium phosphide, and lithium niobate—in five categories—integration density, loss per bend, passive and active optics, and photon sources.

Our density and loss metrics are weighted by published bend radii (r) and propagation loss (α) [133]–[138]. We use bend footprint and loss per bend (proportional to r^2 and αr, respectively) as indicators of density and total loss. Our passive optics metric is based on yield and repeatability, whereas our active optics metric depends on modulator bandwidth and loss. We include an estimation of each platform’s photon source performance and compatibility. Silicon photon-pair sources based on SFWM have been well-explored, revealing themselves to be versatile but vulnerable to TPA-based effects (see Section II-A). Recent results in silicon nitride [39], [139] and direct-write silica waveguides [140]–[142], also based on SFWM, show promise. Gallium arsenide and lithium niobate both host photon-pair sources based on SPDC [143]–[146]; GaAs also supports sources of true single photons, from InAs and InGaAs quantum dots [22], [89], [147]. Finally, indium phosphide has the advantage of a mature classical photonics ecosystem, including an electrically pumped laser; for QKD systems requiring just one photon at a time, this laser is an adequate replacement [20]. We exclude detectors from this list, as the current dominant technology (the SNSPD) has now been demonstrated with high performance integrated with all the above material systems, with the exception of indium phosphide.

Silicon (unsurprisingly) scores well overall in our comparison. It has natural strengths in density, loss, and passives, and weaknesses in actives and photon sources, due to lack of a high-speed modulator with low-loss, and lack of a high purity and heralding efficiency photon-pair source.

**IV. FUTURE OF QUANTUM OPTICS IN SILICON**

In the remainder of this review, we discuss the challenges facing silicon quantum photonics, and potential avenues of enquiry to overcome them.

**A. Integration**

The main challenge for the future quantum photonic engineer will be to take all the various components outlined in Section II, and combine them on a single die or in a single package. Several obstacles lie before him or her: all components must operate in a common environment, and different components pull this
environment in different directions (e.g., cold superconducting detectors versus hot CMOS electronics); all components must use manufacturing processes which are mutually compatible; and this future system must be packageable, so as to be robust and reliable.

It is increasingly likely that the future of quantum photonics will be cold. There has been no demonstration of a waveguide-coupled detector operating at room-temperature.\(^4\) SNSPDs operate around 2 K, and offer very high performance. Superconducting detectors present a devil’s bargain: they represent a near-perfect single-photon detector, with the caveat that everything else must work at cryogenic temperatures. This caveat casts doubt on the thermal tuners used in most demonstrations to date; it also presents challenges for silicon’s carrier-based modulators. It requires careful accounting for system energy budgets. Packaging and testing must adapt to an environment of enormous temperature swings. The absence of materials data at low temperatures makes the development of novel materials more difficult. The unforeseen and unforeseeable effects of this transition are legion.

More pragmatically, silicon quantum photonics at low temperatures may hold several advantages. Compact integrated optics can be readily fit into the confines of a cryostat, in contrast with bulk- or fibre-optic systems. Nonlinear optical effects are largely temperature-independent: \(\chi^{(2)}\) effects work nearly as well at low temperature [148]. SFWM photon-pair sources, as well as XPM and FWM, will continue to work. Thermal-expansion-based straining, used to produce \(\chi^{(2)}\) in silicon [103], could benefit from the larger deposition-to-operation temperature difference; \(\chi^{(2)}\) and \(\chi^{(3)}\) electro-optic modulators could be invaluable for low-temperature switching. Finally, the effort expended to make silicon photonics athermal would be unnecessary: silicon’s low-temperature thermo-optic coefficient is 10 000 times smaller than its room-temperature value [148], [149]. We can be optimistic that silicon photonics can accommodate either front- or back-end integrated electronics, for implementing the feedforward control required (Fig. 9). CMOS electronics can be made to work at cryogenic temperatures [150]–[152], and superconducting electronics [153], [154] could serve a naturally complementary role.

Cold silicon quantum photonics certainly presents challenges, but it comes with benefits too. The biggest prize of all, though, is access—enabled by integrated single-photon detection—to previously unimaginable system architectures. The multiplexing of single photons [155] is only the first of these architectural shifts.

Several components that we have discussed require materials and methods which are well beyond those present in a typical CMOS foundry. SNSPDs call for sputtered layers of exotic metals; fast, low-loss modulators may require exotic materials like barium titanate [107] or nonlinear polymers [106]; and grating fibre-to-chip couplers benefit from engineered substrates [156], [157]. All these materials and processes have been integrated with silicon photonics, but inevitably there will be challenges in integrating them with each other.

B. Two-Photon Absorption

Two-photon absorption dominates discussions of nonlinear optics in silicon. TPA is occasionally useful (e.g., [158]), but is more often problematic. When we want to use the Kerr effect directly—via SPM, XPM, or FWM—the presence of nonlinear loss fundamentally reduces efficiency. In the case of SFWM, this translates to a fundamental reduction in heralding efficiency, which will become intolerable as time goes on. Furthermore, when two photons are absorbed, they excite an electron into the conduction band. This electron then adds further loss, via free-carrier absorption, and modifies the refractive index, via free-carrier dispersion (FCD) and by the thermo-optic effect, when it eventually relaxes. In addition to improving heralding, banishing TPA will yield several corollary benefits in techniques which will become feasible: XPM all-optical switches [158], [159]; FWM frequency-conversion [160]; Raman lasing [161]; and FWM and Raman parametric amplification [162], [163].

1) Effect on Heralding Efficiency: As pointed out by Husko et al. in [32], cross-two-photon absorption (XTPA) decreases the heralding efficiency of photon-pair sources in silicon. Assuming phase-matched SFWM with weak squeezing, the extra uncorrelated loss on the signal channel, relative to the idler channel, for a given pair-generation probability \(p\), is

\[
\eta_{\text{XTPA}} \approx \frac{1}{1 + \frac{\alpha_2}{k_0 n_2} \sqrt{\frac{p}{\Delta t \Delta \nu_c}}},
\]

where \(\alpha_2\), \(n_2\), \(k_0\), \(\Delta t\), and \(\Delta \nu_c\) are respectively: the TPA coefficient, nonlinear refractive index, vacuum wavenumber, pump pulse time, and collection bandwidth. This extra loss, plotted in Fig. 11, is intrinsic to SFWM in silicon at 1.55 \(\mu\)m, and represents a limit on the attainable heralding efficiency.

Solutions to the TPA problem fall into two broad categories: a switch to a photon-pair source material which exhibits no or greatly reduced TPA; or a switch to a longer wavelength, beyond silicon’s two-photon band edge. We discuss these two categories below.

\(^4\)The development of a room-temperature detector is not inconceivable, but may prove challenging; a waveguide-coupled, CMOS-compatible germanium avalanche photodiode [74] may be a good candidate.
2) **Larger Bandgap Materials:** Large $\chi^{(3)}$ non-linearity can be obtained from organic materials and can be leveraged using slot waveguides [164]. Amorphous silicon is also a promising candidate [158]. However Raman scattering over a large bandwidth will accompany pair generation in these amorphous materials which will have to be operated accordingly. The addition of $\chi^{(2)}$ materials such as GaN [165] or AlN [105] may also provide the option of using SPDC instead of SFWM to generate photon pairs.

3) **Longer Wavelengths:** A shift to longer wavelengths, nearer silicon’s two-photon bandgap (2.2 $\mu$m), may be the simplest approach. 2-$\mu$m SOI devices maintain a high nonlinearity, while almost completely eliminating nonlinear absorption [166], allowing silicon to keep its nonlinear-optical crown [167]. Complexity is shifted entirely to the optics: new pump sources, and detectors must be developed to operate at these longer wavelengths. What makes this approach credible are recent developments on both fronts: high-powered thulium- and holmium-doped fibre lasers are becoming available, and measurements on the amorphous superconductors are showing sensitivity to long wavelengths [168]–[170]. Indeed, SNSPDs were first developed to detect millimetre-wave signals [171]. Provided these optical challenges can be met, the long-wavelength route could be a promising one.

V. **CONCLUSION**

Quantum photonic applications are beginning to come into technological reach, as evidenced by the recent demonstration of a fully chip-based QKD system [20], relying on an indium phosphide transmitter and a silicon oxynitride receiver. Commercialization of this technology could rapidly find that, for mass-deployment, silicon photonics is the only route to the associated mass-manufacture. Multi-project wafer (MPW) services allow many small research teams to share the cost of state-of-the-art silicon manufacturing for small-batch and prototype devices. These services give the research community the ability to test and prove small-scale quantum photonic designs without needing the resources to fabricate or commission entire wafers. In the near and medium terms, these services could seriously accelerate the development of silicon quantum photonics, allowing the community to incrementally address the remaining issues, and to test strategies for integration and packaging without large-scale investment.

Our rising quantum photonic technological capability, and the falling thresholds for linear optical quantum computation (e.g., [64], [130], [173]), are poised to meet in the middle. At its core, silicon technology provides the only viable route to assembling systems of millions of components. So, in addition to its natural technical advantages, it may represent the only option for constructing quantum computers which run on light. In this review, we have provided an overview of the technological modules required by future quantum photonic systems, and we have elaborated on the strengths, weaknesses, context, and future challenges for silicon as a springboard for photonic quantum technologies.

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